

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Masaki Tamaru et al.

Title: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME UTILIZING PERMITTIVITY OF AN INSULATING LAYER TO PROVIDE A DESIRED CROSS CONDUCTIVE LAYER CAPACITANCE PROPERTY

Docket No.: 32811US1

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.98, applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. Per 37 C.F.R. § 1.98(d), copies of the references are not required since each reference listed was cited in application Serial No. 09/616,086, filed on July 14, 2000.

Respectfully submitted,

PEARNE & GORDON LLP

By:



Ronald M. Kachmarik, Reg. No. 34512

1801 East 9th Street
Suite 1200
Cleveland, Ohio 44114-3108
(216) 579-1700

Date: November 26, 2003

INFORMATION DISCLOSURE CITATION
BY APPLICANT
(USE SEVERAL SHEETS IF NECESSARY)

Page 1 of 1

APPLICANT:
Masaki Tamaru et al.FILING DATE:
HerewithGROUP ART UNIT:
TBD

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date
	A	4,654,689	3/1987	Fujii	257	296	
	B	4,954,877	9/1990	Nakanishi et al.	257	659	
	C	5,311,048	5/1994	Takahashi et al.	257	207	
	D	5,394,008	2/1995	Ito et al.	257	666	
	E	5,594,279	1/1997	Itou et al.	257	758	
	F	5,598,029	1/1997	Suzuki	257	665	
	G						
	H						
	I						
	J						
	K						
	L						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Subclass	Translation
	M						
	N						
	P						
	Q						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	R	
	S	
	T	

Examiner:

Date Considered

*Examiner: Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.